

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	Variable Capacitor Using MOS Gated Diode with Multiple Segments to Limit DC Current		
Application Number :			
Date :			
First Named Applicant:	Min Cao		
Confirmation Number:			
Attorney Docket Number:	PS-62A		
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Submitted By:		Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files
us-fee-sheet	PS62Ae-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-request	PS62Ae-usrequ.xml us-request.dtd us-request.xsl
us-declaration	PS62oath.tif
application-body	PS62a-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd fgPS62e1.TIF fgPS62e2.TIF fgPS62e3.TIF fgPS62e4.TIF fgPS62e5.TIF fgPS62e6.TIF fgPS62e7.TIF fgPS62e8.TIF
Comments	